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APPLICANTS

Shoriki Narita, Hirakata-shi, JAPAN;

Masahiko Ikeya, Sakai-shi, JAPAN;
 Yasutaka Tsuboi, Hirakata-shi, JAPAN; Takaharu Mae, Hirakata-shi, JAPAN;
 Shinji Kanayama, Kashihara-shi, JAPAN;

** CONTINUING DATA *****
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** FOREIGN APPLICATIONS *****
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** 12/03/2001

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Verified and Acknowledged	<i>12/23/04</i> Examiner's Signature <i>12/23/04</i> Initials	JAPAN	18	15 ✓	2 ✓

ADDRESS

000513
 WENDEROTH, LIND & PONACK, L.L.P.
 2033 K STREET N. W.
 SUITE 800
 WASHINGTON , DC
 20006-1021

TITLE

Method and apparatus for correcting inclination of IC on semiconductor wafer

FILING FEE	FEES: Authority has been given in Paper	<input type="checkbox"/> All Fees
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